IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Johannes Roelof Gerardus de Vries

Serial No.: 08/422,262/

Filed: April 14, 1995

For: DATA PROCESSING CIRCUIT, MULTIPLIER UNIT WITH PIPELINE, ALU AND SHIFT REGISTER UNIT FOR USE IN A DATA PROCESSING CIRCUIT

Date: August 11, 1997

Group Art Unit: 2306

Examiner: R. Dolan

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AMENDMENT TRANSMITTAL LETTER - FEE COMPUTATION

Hon. Commissioner of Patents and Trademarks
Washington, D.C. 20231

        Transmitted herewith is an amendment in the above-identified application.

._____"Small Entity" status (37 C.F.R. §1.9 & §1.27) established
        _____previously   _____by enclosed verified statement.

OFGS Check No. _____, which includes the fee of $ -0- calculated below, is attached.

NO. CLAIMS AFTER AMENDMENT		HIGHEST NO. PREVIOUSLY PAID FOR		EXTRA PRESENT		RATE	ADDIT. FEE
TOTAL 21	MINUS	22	* =		x	($11 SE or $22)	$
INDEP. 6	MINUS	7	** =		x	($40 SE or $80)	$
FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM					x	($130 SE or $260)	$
* not less than 20	** not less than 3					TOTAL	$0.00

In the event the actual fee is greater than the payment submitted or is inadvertently not enclosed or if any additional fee during the prosecution of this application is not paid, the Patent Office is authorized to charge the underpayment to Deposit Account No. 15-0700.

If this communication is filed after the shortened statutory time period had elapsed and no separate Petition is enclosed, the Commissioner of Patents and Trademarks is petitioned, under 37 C.F.R. §1.136(a), to extend the time for filing a response to the outstanding Office Action by the number of months which will avoid abandonment under 37 C.F.R. §1.135. The fee under 37 C.F.R. § 1.17 should be charged to our Deposit Account No. 15-0700.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents and Trademarks, Washington, D.C. 20231, on August 11, 1997
Matthew B. Dernier
Name of Registered Representative

Signature
August 11, 1997
Date of Signature

Respectfully submitted,

Matthew B. Dernier
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RESPONSE UNDER 37 C.F.R. §1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2306

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Johannes Roelof Gerardus de Vries

Serial No.: 08/422,264

Filed: April 14, 1995

New York, New York

Date: August 11, 1997

Group Art Unit: 2306

Examiner: R. Dolan

For: DATA PROCESSING CIRCUIT, MULTIPLIER UNIT WITH PIPELINE,
ALU AND SHIFT REGISTER UNIT FOR USE IN A DATA PROCESSING
CIRCUIT

.Hon. Commissioner of Patents
   and Trademarks
Washington, D.C.   20231

AMENDMENT PURSUANT TO 37 C.F.R. §1.116

Sir:

In response to the final Office Action mailed May 9,
1997, please reconsider the above-identified application amended
as follows and in light of the following remarks:

IN THE CLAIMS:

Please cancel claims 4 and 19 without prejudice.

Please amend the claims as follows:

1.   (Twice Amended) A circuit for processing integer
data for graphic image processing applications, comprising:

a multiplier unit having a pipeline for multiplying
integer data words of 8 bits or multiples thereof, the pipeline
being adjustable to the length of the integer data words to be
multiplied;

an arithmetic logic unit (ALU) for performing
arithmetic operations on integer data words of 8 bits or